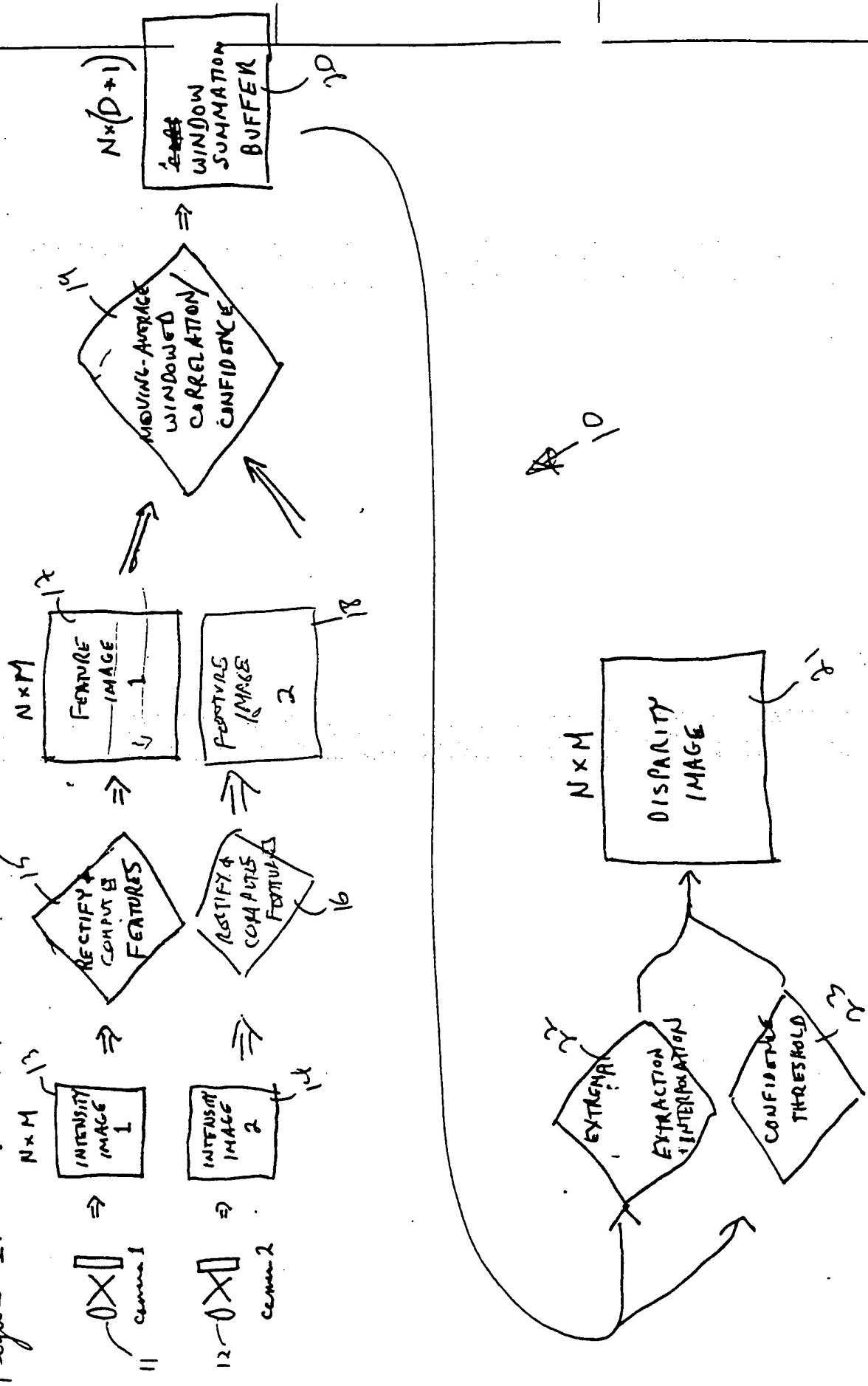


Figure 1.



Correlation operation  
Correlation operation

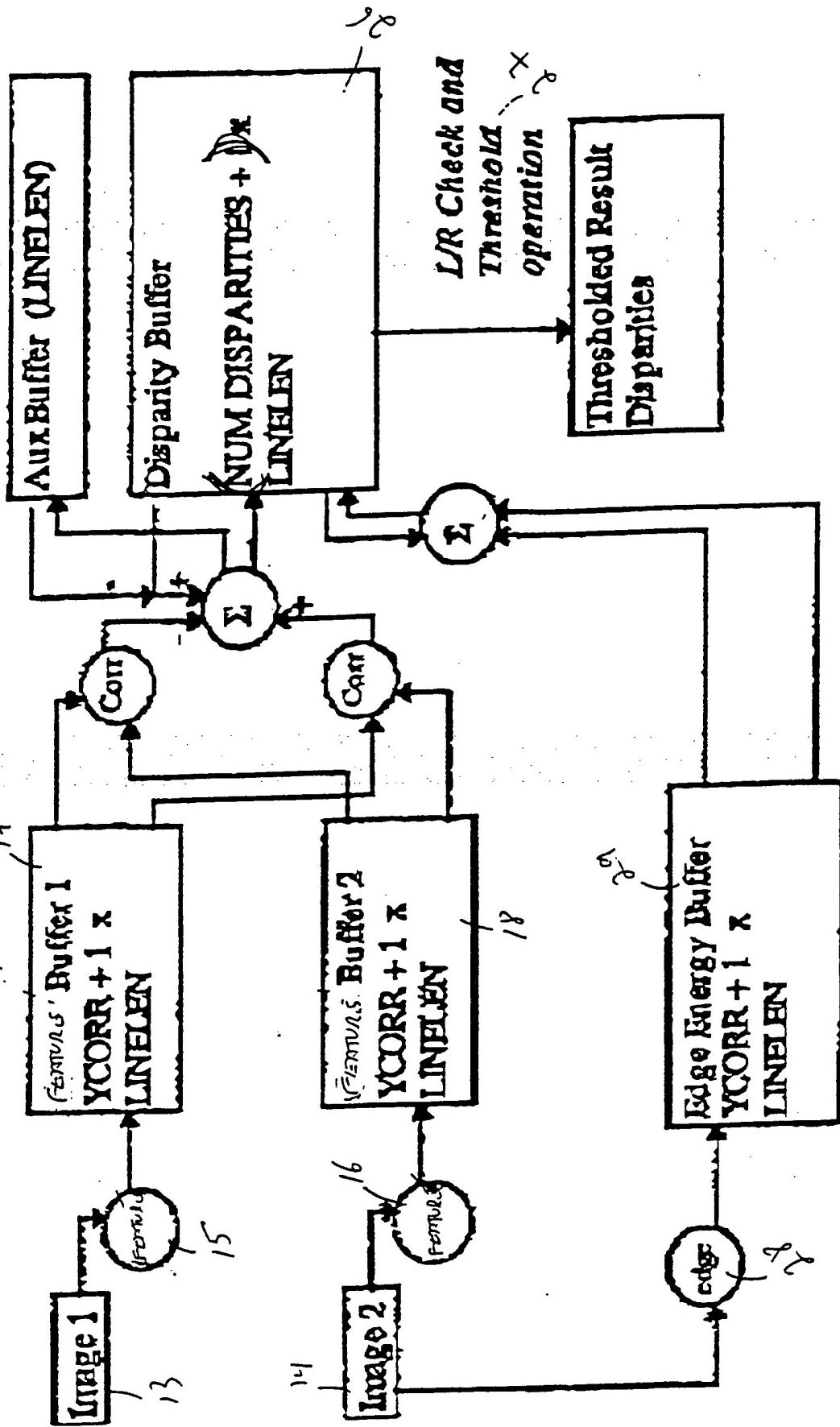
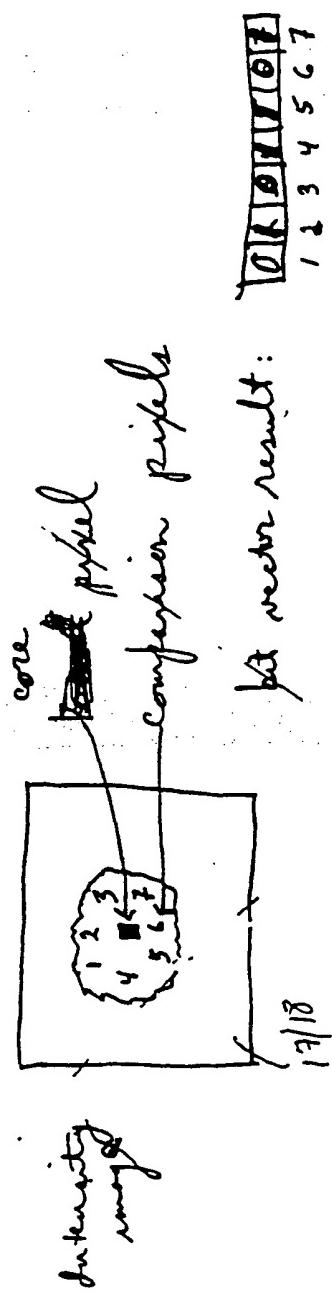


Figure 1

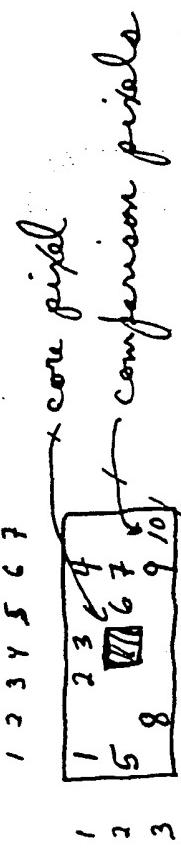
Figure 3

Fig. 3(a)



bit vector result:  $\begin{matrix} 0 & 1 & 0 & 1 & 0 & 1 \\ 1 & 2 & 3 & 4 & 5 & 6 & 7 \end{matrix}$

Fig. 3(b)



bit vector result:  $\begin{matrix} 1 & 2 & 3 & 4 & 5 & 6 & 7 \\ 1 & 5 & 8 & 6 & 9 & 10 \end{matrix}$

Figure 4

Fig. 4 (a)

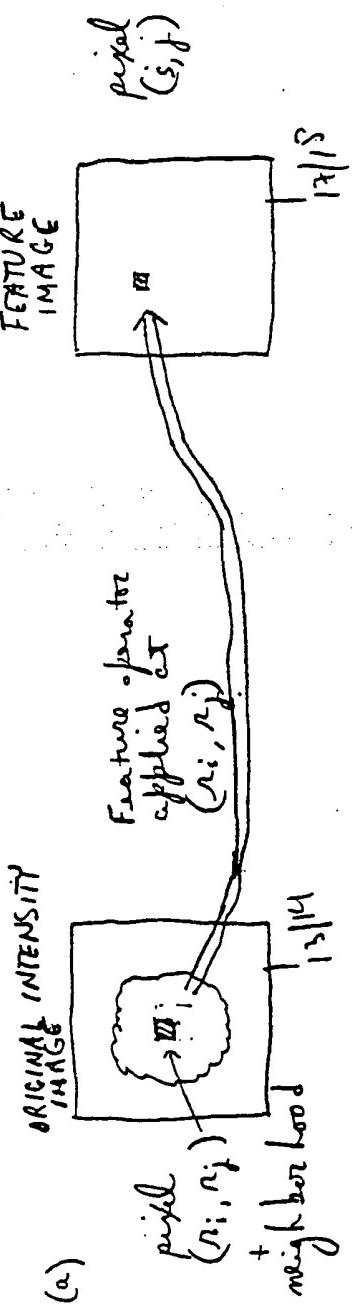


Fig. 4 (b)

operator shifted ( $\frac{1}{4}, \frac{1}{2}$ )

1	2	3	4
5	6	7	8
9	10		
1	2	3	4

Original operator

1	2	3	4
5	6	7	8
9	10		

Figure 4

BUFFER SIZES.....

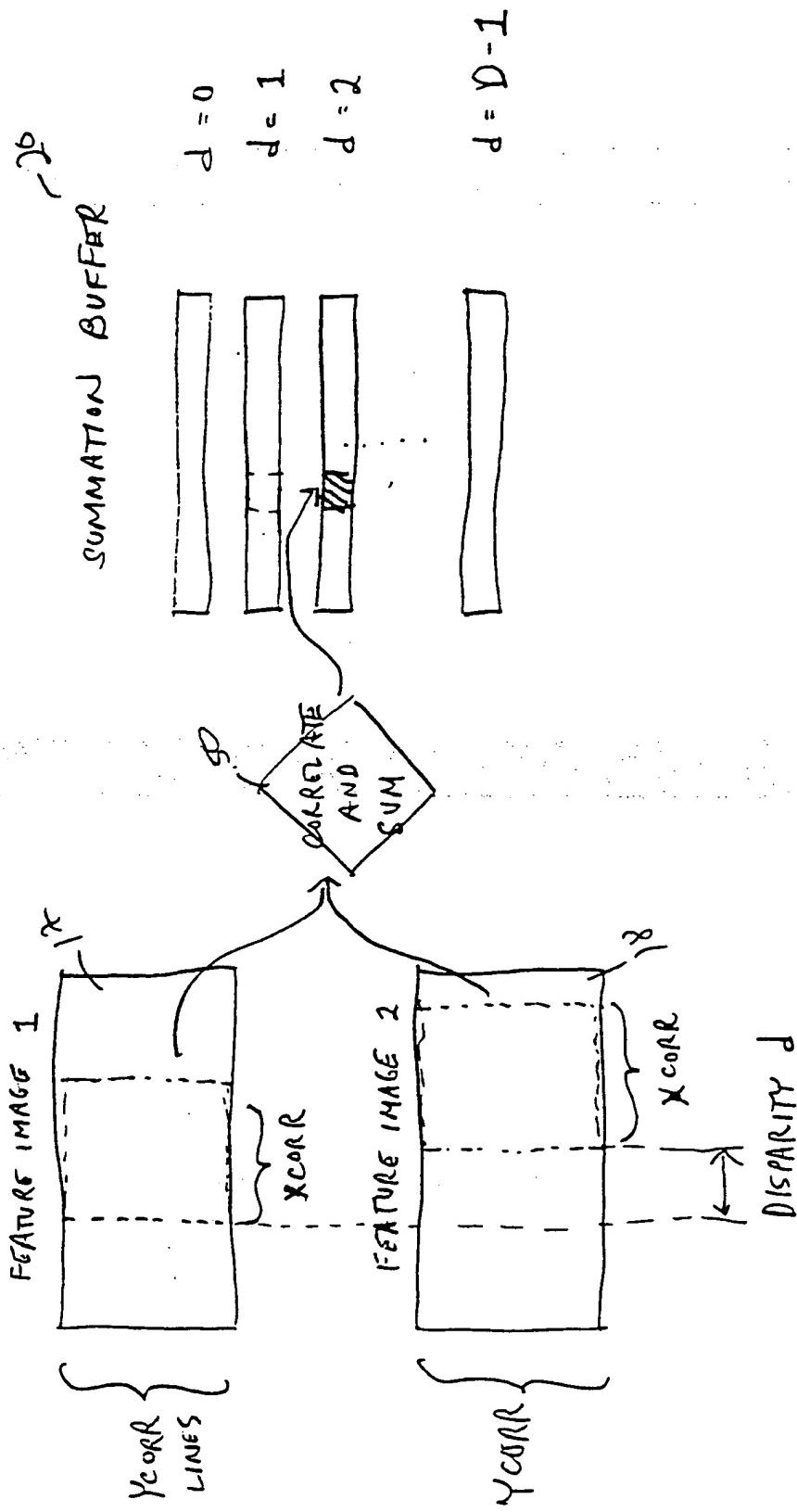


Figure 6

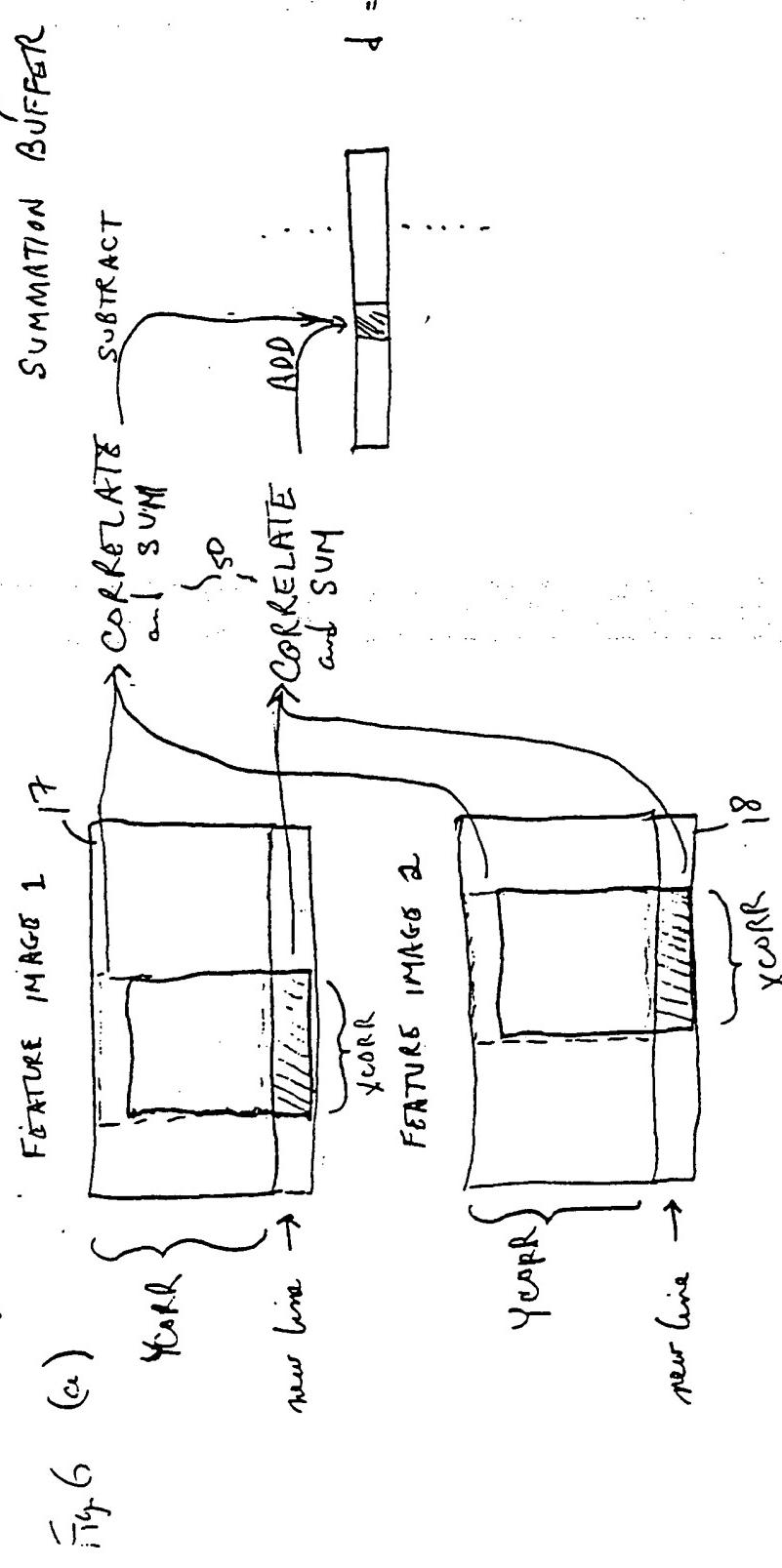


Fig-6 (b)

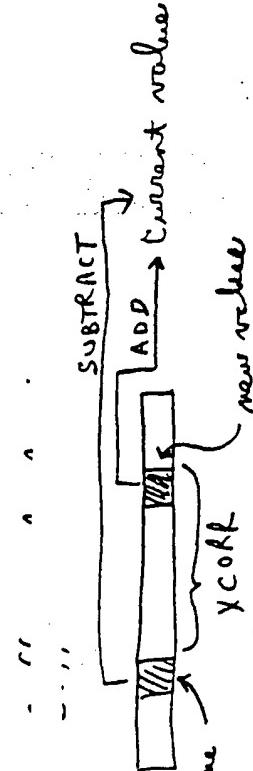
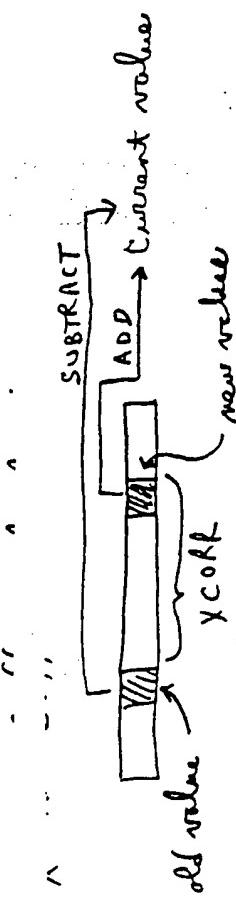


Figure 7

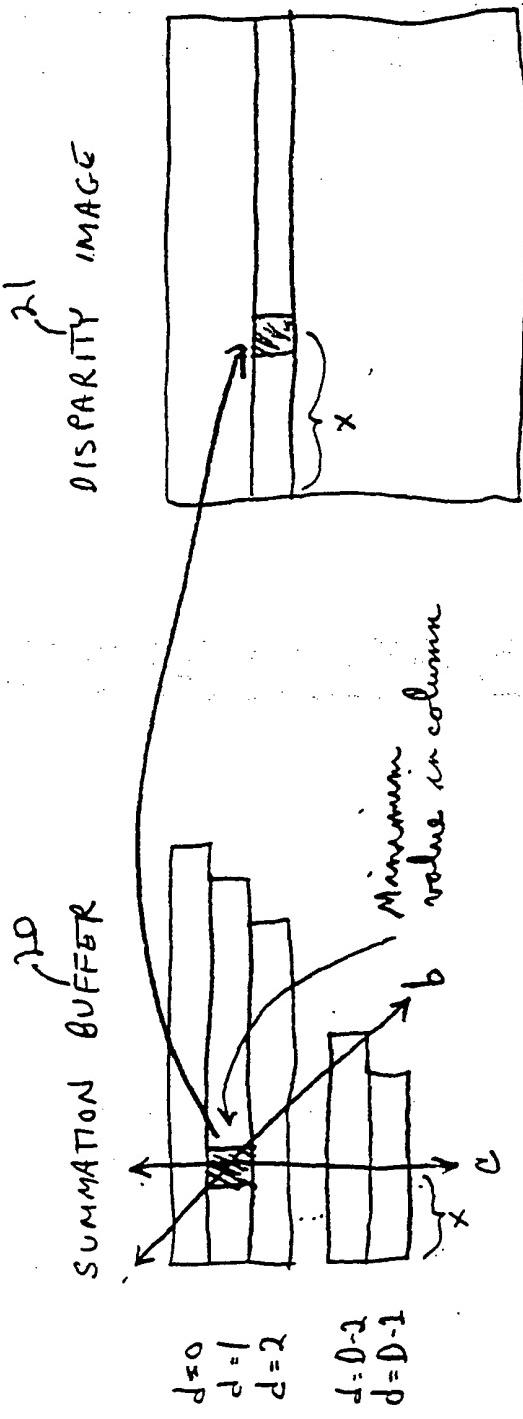


Figure 8

